

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Patent Application of:

Shunpei YAMAZAKI et al.

Serial No. 09/832,867

Filed: April 12, 2001

For: LIGHT EMITTING DEVICE

COMPRISING THIN FILM

TRANSISTOR WITH DISTINCT

POSITION OF GATE ELECTRODE

AND IMPURITY REGIONS

) Group Art Unit: 2822

) Examiner: M. Lewis

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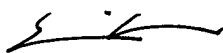
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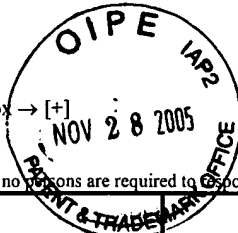
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				Application Number	09/832,867
				Filing Date	April 12, 2001
				First Named Inventor	Shunpei YAMAZAKI et al.
				Group Art Unit	2822
				Examiner Name	M. Lewis
				Attorney Docket Number	0756-2294
Sheet	1	of	2		

U.S. PATENT DOCUMENTS						
Examiner Initials <sup>*</sup>	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> <i>(if known)</i>			
		2001/0055841		Yamazaki et al.	12/27/2001	
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FOREIGN PATENT DOCUMENTS								
Examiner Initials <sup>*</sup>	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
		JP	08-160464			06/21/1996		Abst.
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		R. SHIMOKAWA, et al., <i>Characterization of High-Efficiency Cast-Si Solar Cell Wafers by MBIC Measurement</i> , Japanese Journal of Applied Physics, May 1, 1988, Volume 27, No. 5, Pages 751-758.		
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Sheet 1 of 3

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Application Number 09/832,867  
Filing Date April 12, 2001  
First Named Inventor Shunpei YAMAZAKI, et al.  
Group Art Unit 2822  
Examiner Name Monica Lewis  
Attorney Docket Number 0756-2294

## FOREIGN PATENT DOCUMENTS

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		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
		JP	04-369271		Takeda et al.	12/22/92		FULL
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		Tanaka, et al., "Ultrafast Operation of V <sub>th</sub> -Adjusted p <sup>+</sup> -n <sup>+</sup> Double-Gate SOI MOSFET'S," IEEE Electron Device Letters, Vol. 15 (Oct., 1994), No. 10, pp. 386-388.	ENG
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